



# SPEC® CFP2006 Result

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## Fujitsu

PRIMERGY TX120 S3, Intel Xeon E3-1240, 3.30 GHz

**SPECfp®\_rate2006 = 118**

**SPECfp\_rate\_base2006 = 115**

CPU2006 license: 19

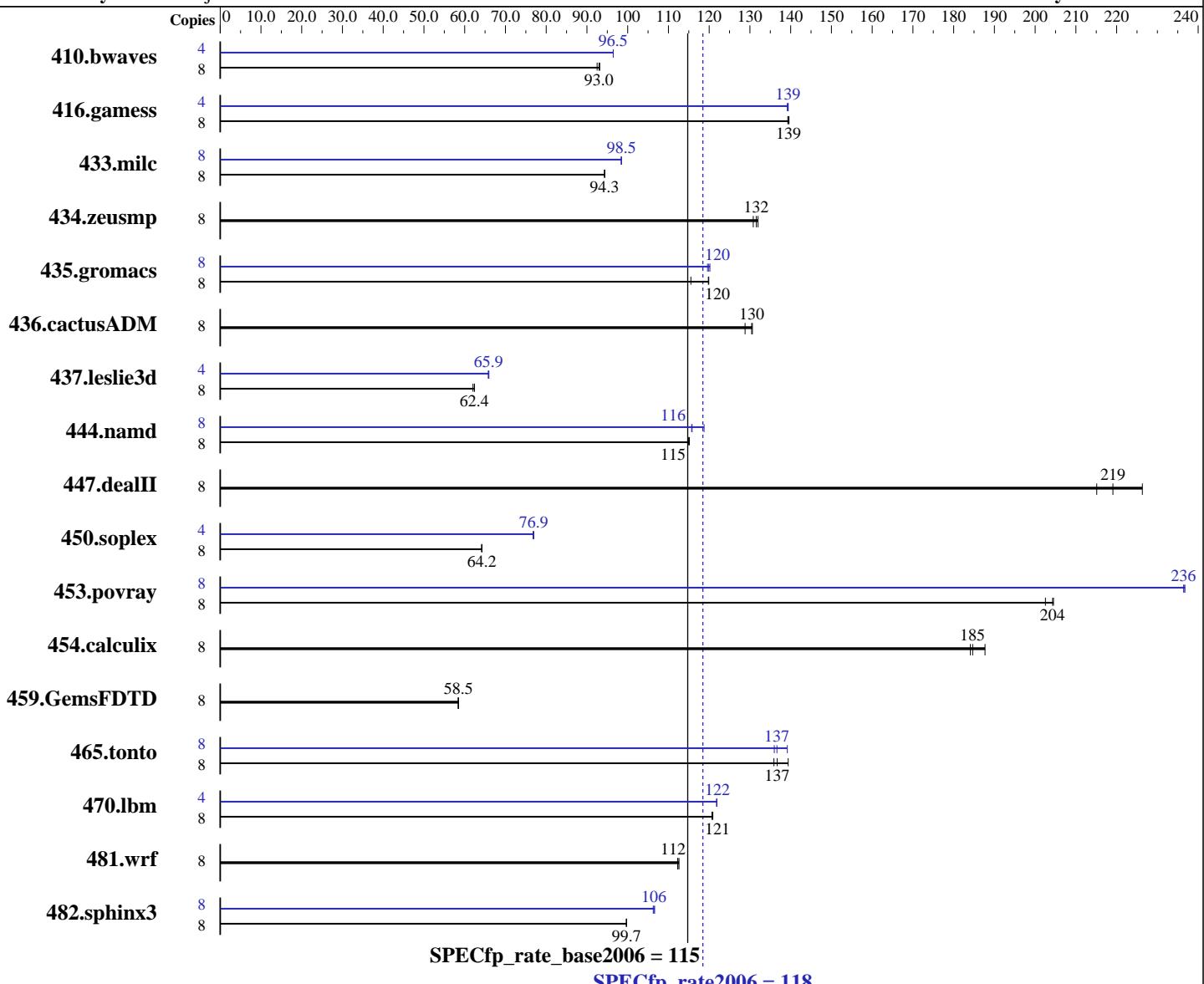
Test date: May-2011

Test sponsor: Fujitsu

Hardware Availability: Jun-2011

Tested by: Fujitsu

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E3-1240  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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## Hardware (Continued)

L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)  
 Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM  
 Other Hardware: --

## Software (Continued)

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1175	92.5	1167	93.1	<u>1169</u>	<u>93.0</u>	4	<u>564</u>	<u>96.5</u>	564	96.5	564	96.5
416.gamess	8	1124	139	<u>1123</u>	<u>139</u>	1122	140	4	<u>562</u>	<u>139</u>	562	139	563	139
433.milc	8	779	94.3	<u>779</u>	<u>94.3</u>	778	94.4	8	746	98.4	<u>746</u>	<u>98.5</u>	746	98.5
434.zeusmp	8	552	132	<u>553</u>	<u>132</u>	557	131	8	552	132	<u>553</u>	<u>132</u>	557	131
435.gromacs	8	<u>477</u>	<u>120</u>	476	120	495	116	8	478	120	475	120	<u>476</u>	<u>120</u>
436.cactusADM	8	742	129	<u>733</u>	<u>130</u>	732	131	8	742	129	<u>733</u>	<u>130</u>	732	131
437.leslie3d	8	<u>1206</u>	<u>62.4</u>	1212	62.0	1205	62.4	4	570	65.9	572	65.8	<u>571</u>	<u>65.9</u>
444.namd	8	<u>558</u>	<u>115</u>	558	115	557	115	8	<u>554</u>	<u>116</u>	555	116	540	119
447.dealII	8	<u>418</u>	<u>219</u>	404	226	425	215	8	<u>418</u>	<u>219</u>	404	226	425	215
450.soplex	8	1040	64.1	1038	64.3	<u>1039</u>	<u>64.2</u>	4	435	76.8	433	77.0	<u>434</u>	<u>76.9</u>
453.povray	8	210	203	<u>208</u>	<u>204</u>	208	205	8	180	236	180	237	<u>180</u>	<u>236</u>
454.calculix	8	358	184	352	188	<u>357</u>	<u>185</u>	8	358	184	352	188	<u>357</u>	<u>185</u>
459.GemsFDTD	8	1451	58.5	1454	58.4	<u>1452</u>	<u>58.5</u>	8	1451	58.5	1454	58.4	<u>1452</u>	<u>58.5</u>
465.tonto	8	579	136	<u>576</u>	<u>137</u>	565	139	8	566	139	579	136	<u>576</u>	<u>137</u>
470.lbm	8	909	121	<u>911</u>	<u>121</u>	911	121	4	<u>451</u>	<u>122</u>	451	122	451	122
481.wrf	8	<u>796</u>	<u>112</u>	796	112	794	113	8	<u>796</u>	<u>112</u>	796	112	794	113
482.sphinx3	8	<u>1564</u>	<u>99.7</u>	1563	99.7	1564	99.7	8	1462	107	1467	106	<u>1465</u>	<u>106</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
 Large pages were not enabled for this run



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## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
This result was measured on the PRIMERGY TX120 S3. The PRIMERGY TX120 S3  
and the PRIMERGY TX140 S1 are electronically equivalent.  
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

## Base Compiler Invocation

C benchmarks:

  icc -m64

C++ benchmarks:

  icpc -m64

Fortran benchmarks:

  ifort -m64

Benchmarks using both Fortran and C:

  icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
  433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
  444.namd: -DSPEC\_CPU\_LP64  
  447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
  465.tonto: -DSPEC\_CPU\_LP64  
  470.lbm: -DSPEC\_CPU\_LP64  
  481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

  -xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

  -xAVX -ipo -O3 -no-prec-div -static -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfss/ -Wl,-hugetlbfss-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

Benchmarks using both Fortran and C:

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## Peak Optimization Flags (Continued)

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Intel-Linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Intel-Linux64.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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